ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention	NON-DESTRUCTIVE EVALUATION OF MICROSTRUCTURE AND INTERFACE ROUGHNESS OF ELECTRICALLY CONDUCTING LINES IN SEMICONDUCTOR INTEGRATED CIRCUITS IN DEEP SUB-MICRON REGIME

Submision Type :   Utility Patent Filing

Application Number:

                 10/711418

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First Named Applicant: Fen Chen

Attorney Docket Number: BUR920040182US1

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us-assignment	BUR920040182Assign2.tif	24108	2004-09-17
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application-body	isotech.ent	5268	2004-09-17
application-body	mathml2.dtd	54588	2004-09-17
application-body	mathml2-qname-1.mod	13225	2004-09-17
application-body	mmlalias.ent	38209	2004-09-17
application-body	mmlextra.ent	7901	2004-09-17
application-body	soextblx.dtd	12870	2004-09-17
application-body	us-application-body.xsl	82504	2004-09-17
application-body	wipo.ent	4956	2004-09-17
application-body-pdf-wrap	BUR920040182ABX-pdf-wrap.xml	765	2004-09-17
abstract-pdf	BUR920040182ABX-abst.pdf	15417	2004-09-17
claims-pdf	BUR920040182ABX-clms.pdf	34980	2004-09-17
description-pdf	BUR920040182ABX-desc.pdf	62495	2004-09-17
drawings-pdf	BUR920040182ABX-draw.pdf	52583	2004-09-17
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